

**Course title:** Digital Design

**Course code:** 63260

**ECTS:** 6

**Professor:** Ratko Pilipović

**Prerequisite knowledge:**

- basic principles of digital logic,
- combinational and sequential logical circuits,
- basics of automaton

**Short course description:**

We instruct students on how computer-aided design tools are used to simulate the VHDL or Verilog design and synthesize the design into actual hardware. The specific behavior of HDL tools is emphasized. We present the design of a digital circuit using optimal approaches. As part of the course, students develop familiarity and confidence with designing, building, and testing digital circuits, including using CAD tools, developing team-building skills, and enhancing technical knowledge through written assignments and design projects.